

Banyan Switched Processor Datapath

Inventors: Michael A. Baxter

Docket No.: 20412-03873

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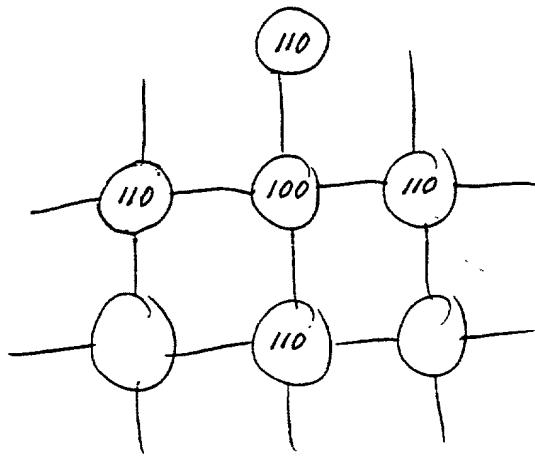
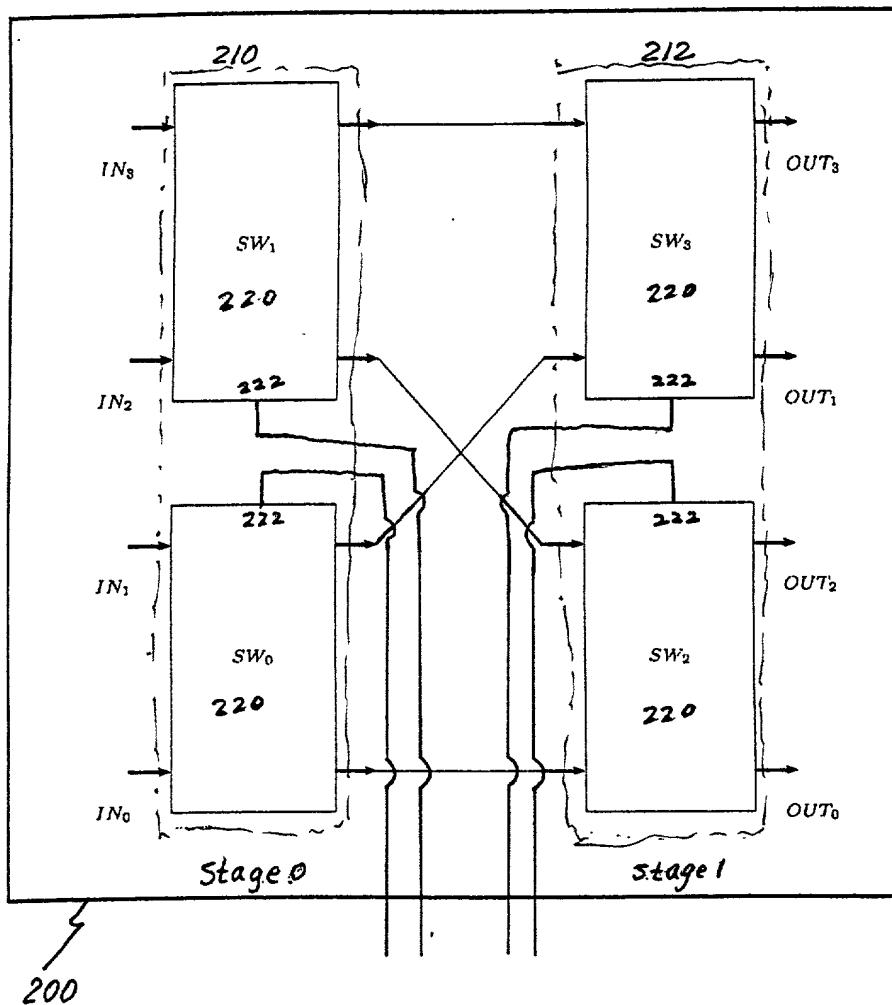


Fig. 1. (Prior Art)



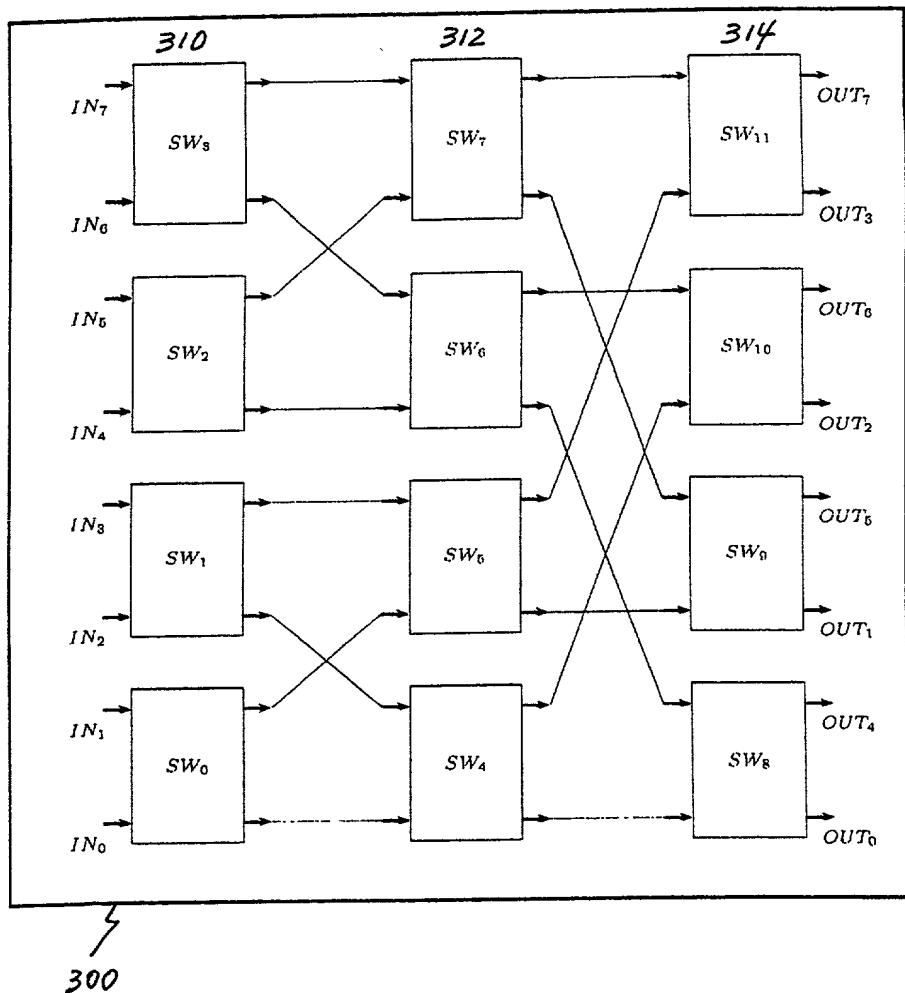


Fig 3

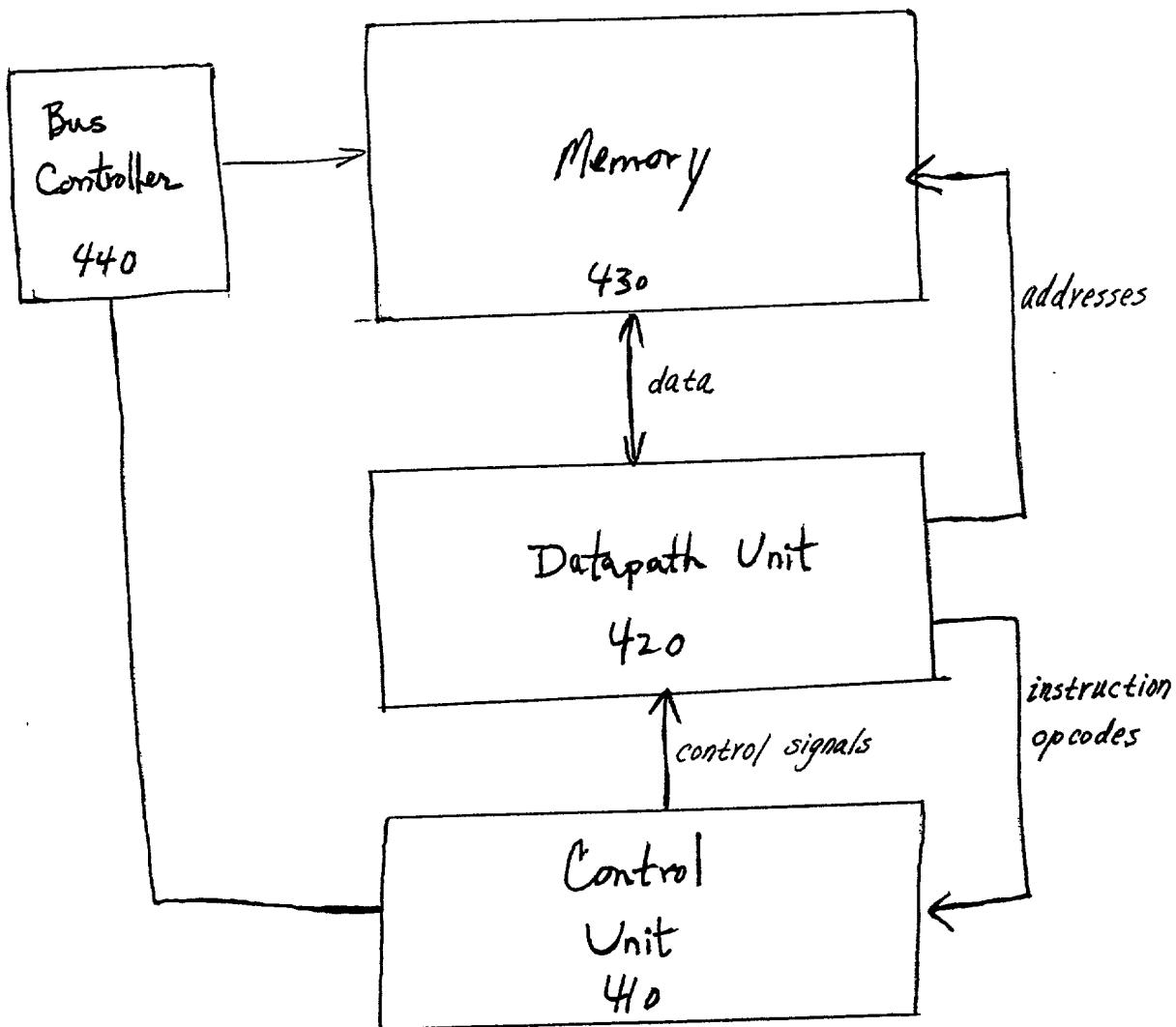


Figure 4

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400

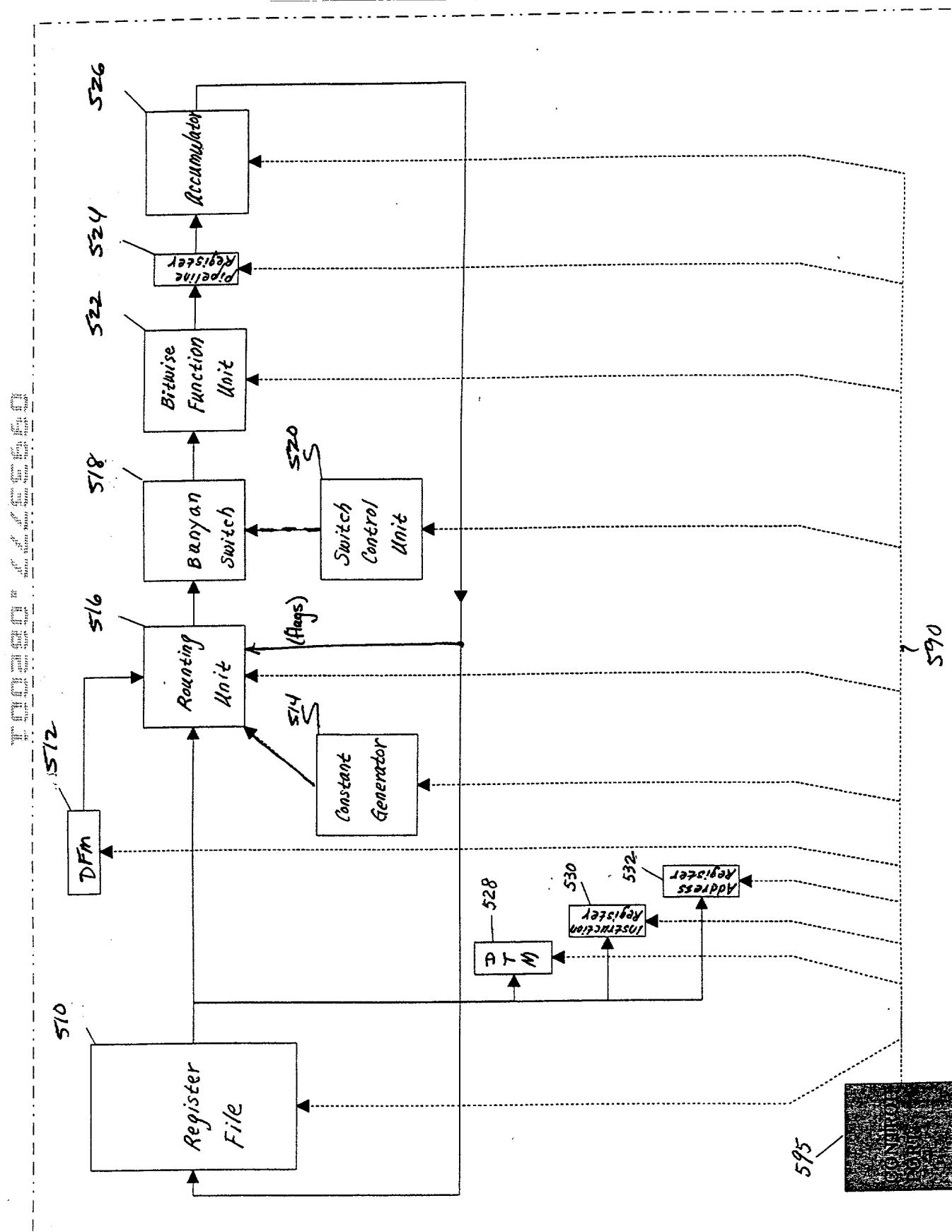


Fig. 5

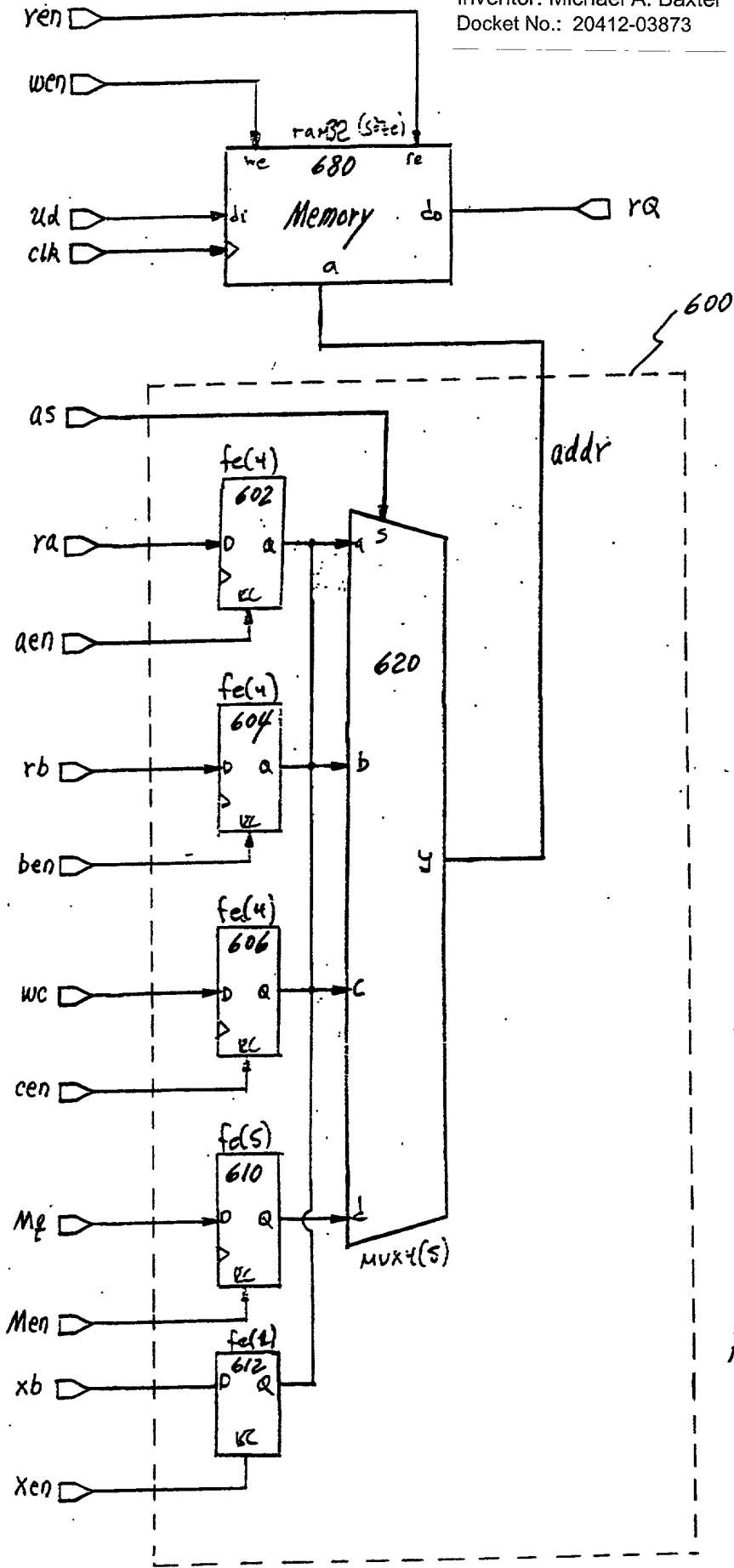


Figure 6A

Function 0	Data from memory, to be passed through switch without rotation.
Function 1	V_k literal for AK instruction, to be passed through switch, without rotation, pre-aligned left of 2^5 .
Function 2	V_k literal for SK instruction, to be passed through switch, without rotation, 2's complemented, pre-aligned left of 2^5 .
Function 3	Insert PCW Flag
Function 4	SK_{16} sign-extended, to be rotated RLC(4), with zero-fill pre-appended.
Function 5	64-bit constants bus.
Function 6	64-bit register file read bus.
Function 7	Force KISA-2 Out

Figure 6B

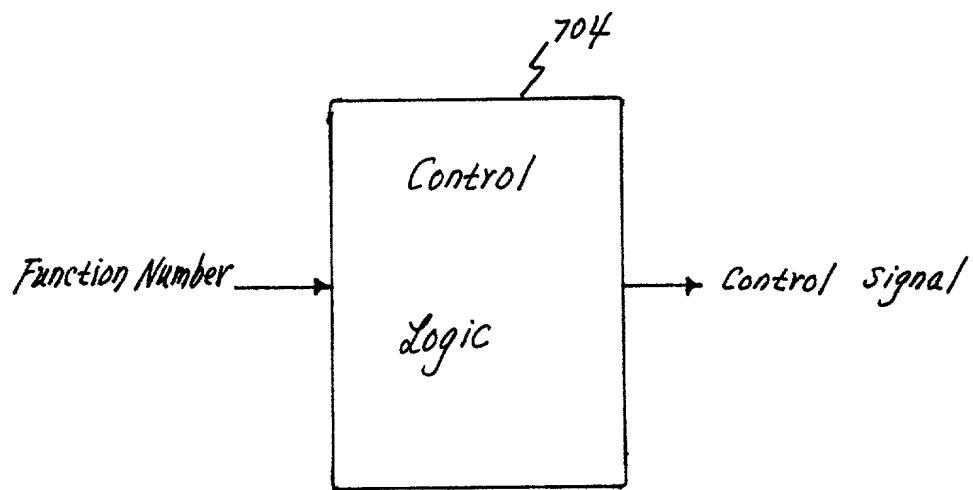
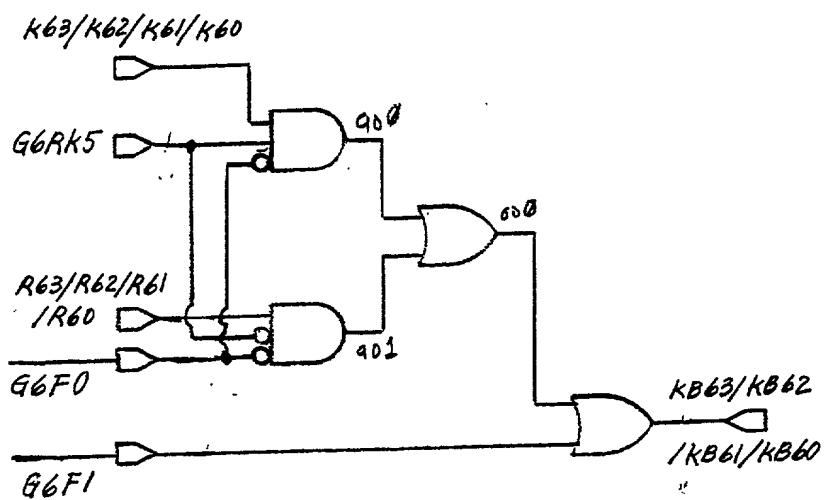


Figure 7A



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Figure 7B

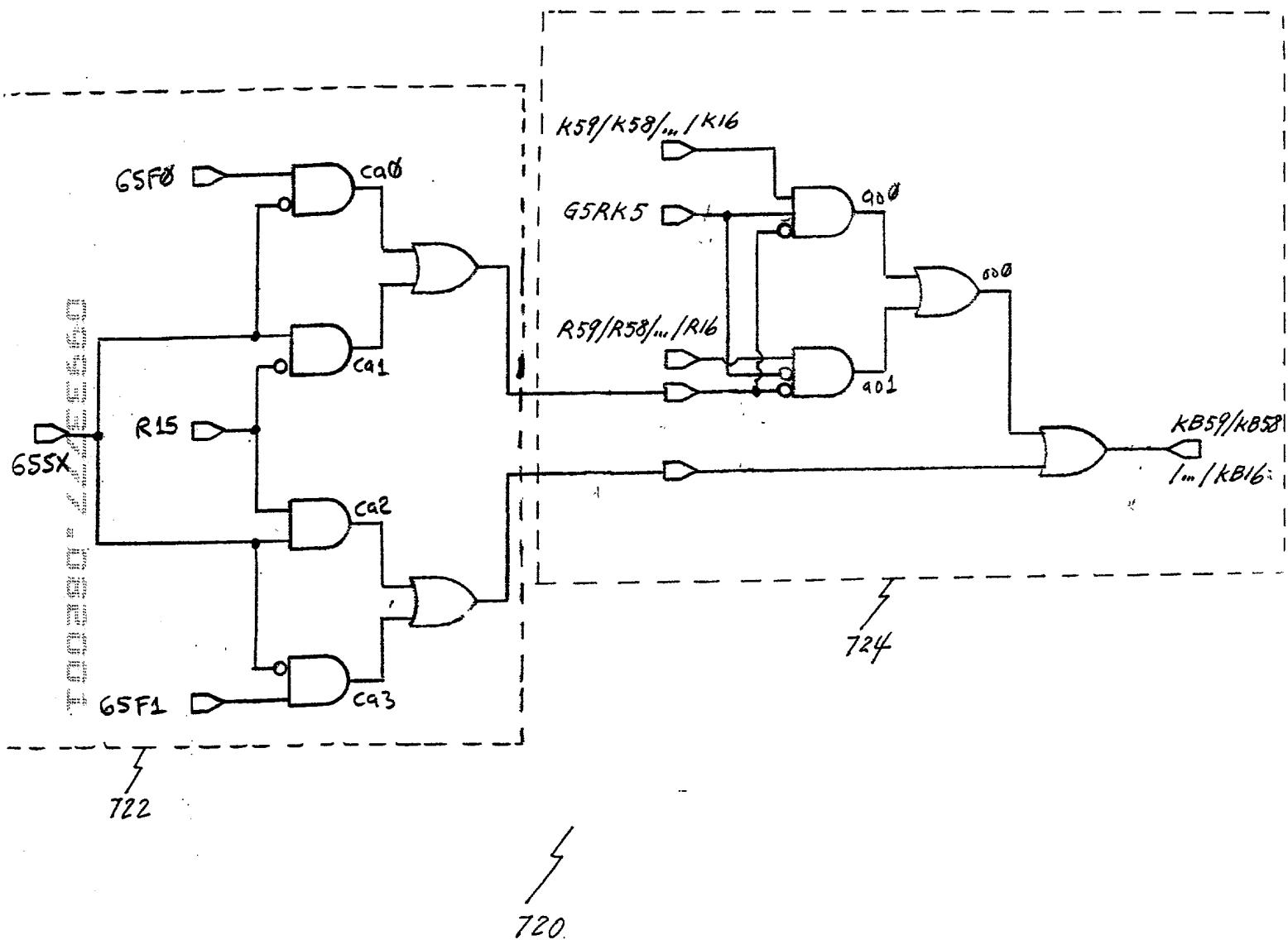


Figure 7C

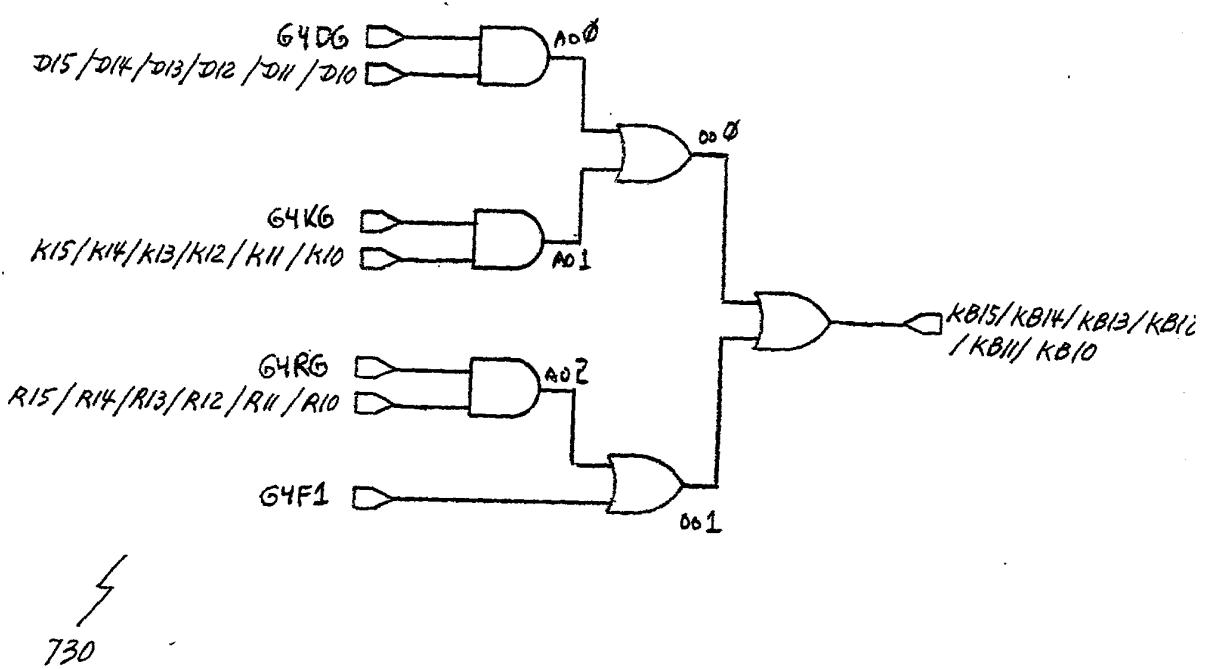


Figure 7D

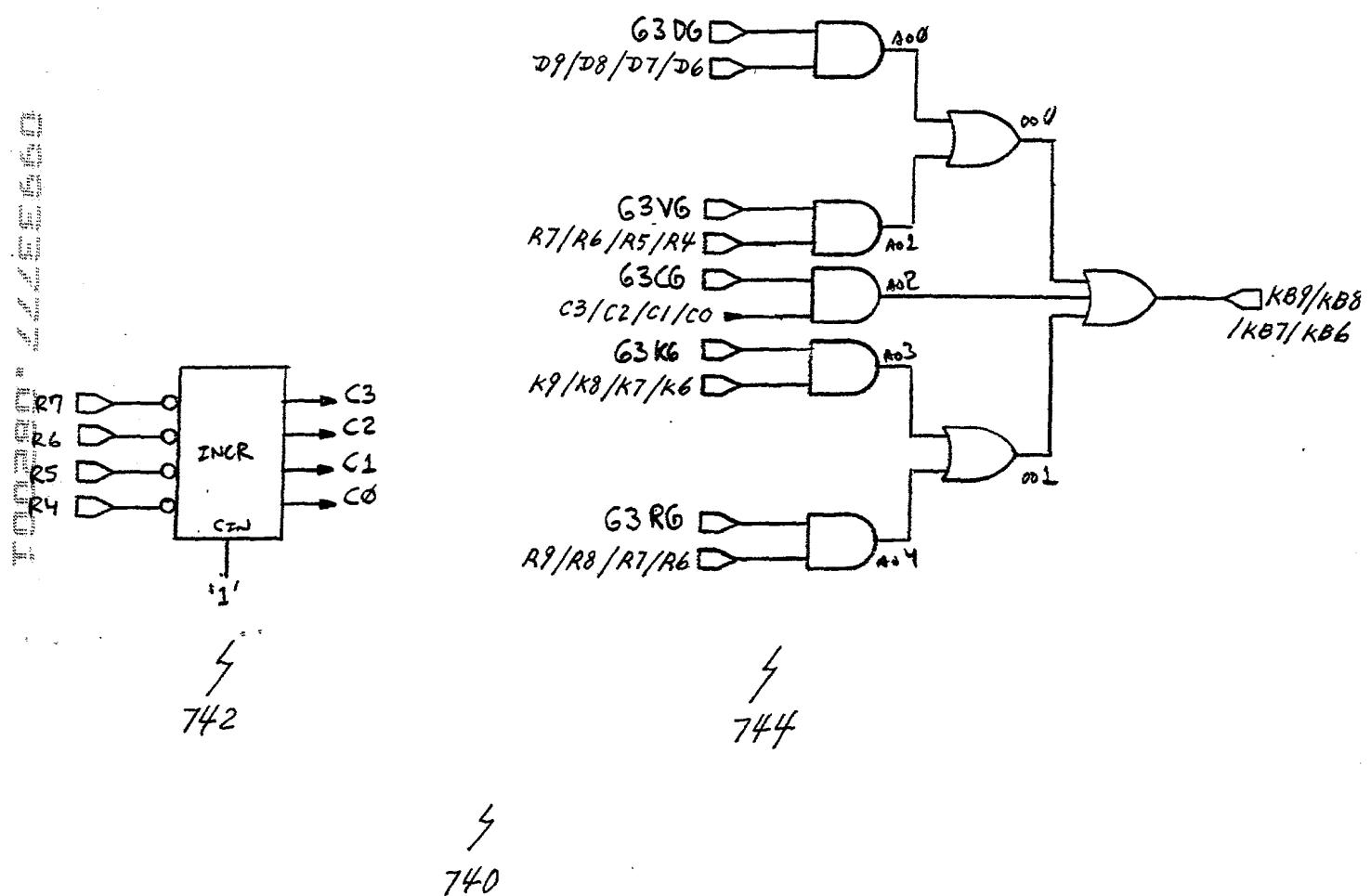
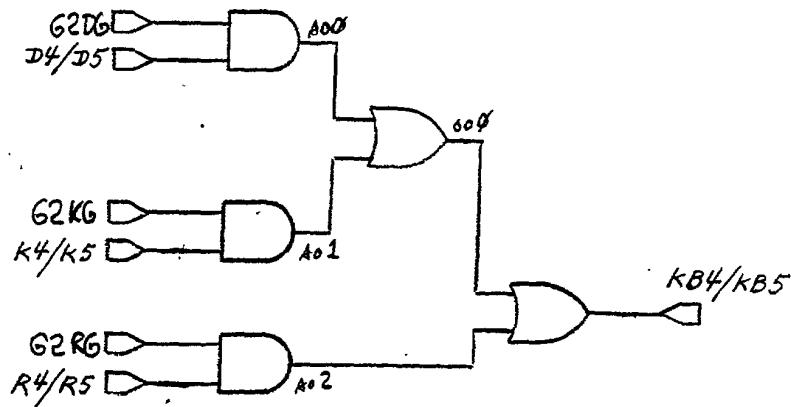


Figure 7E



750

Figure 7F

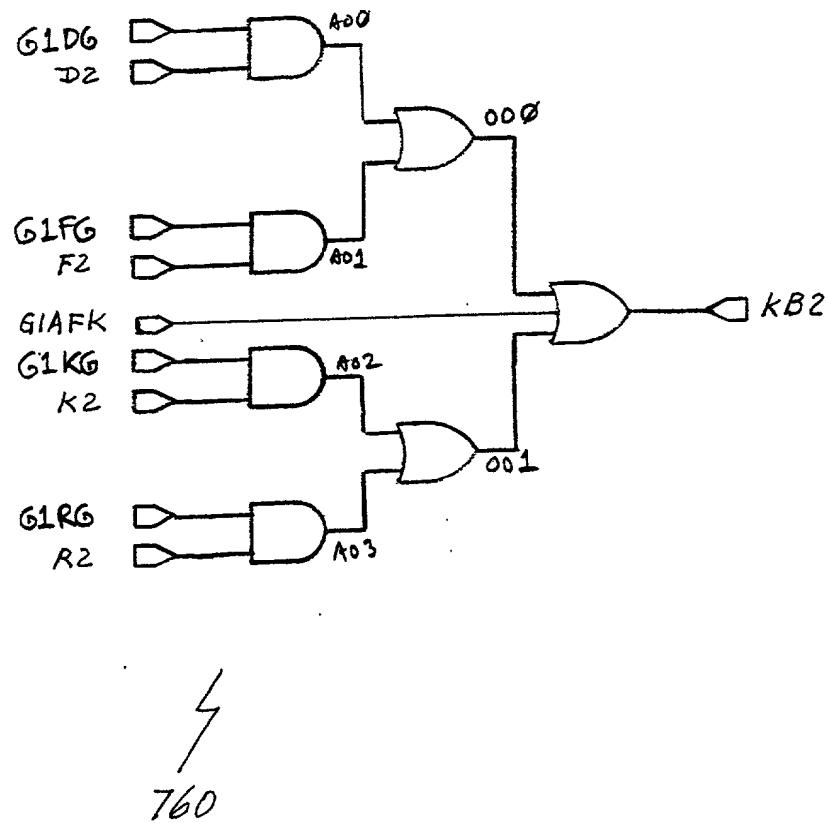


Figure 7G

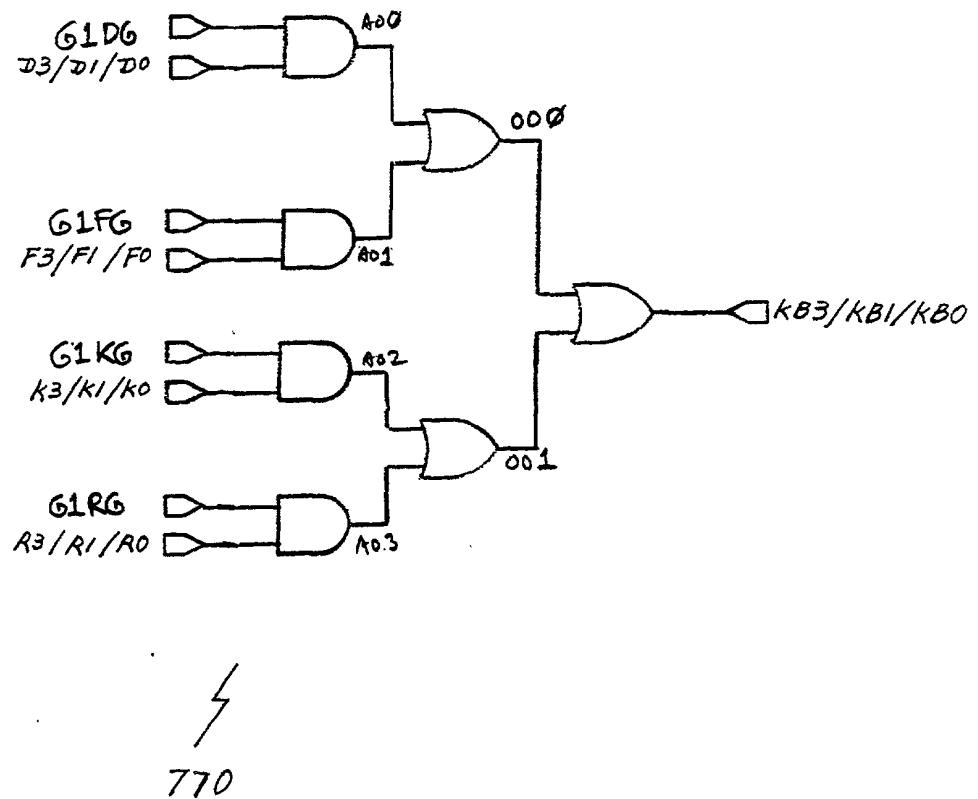


Figure 7H

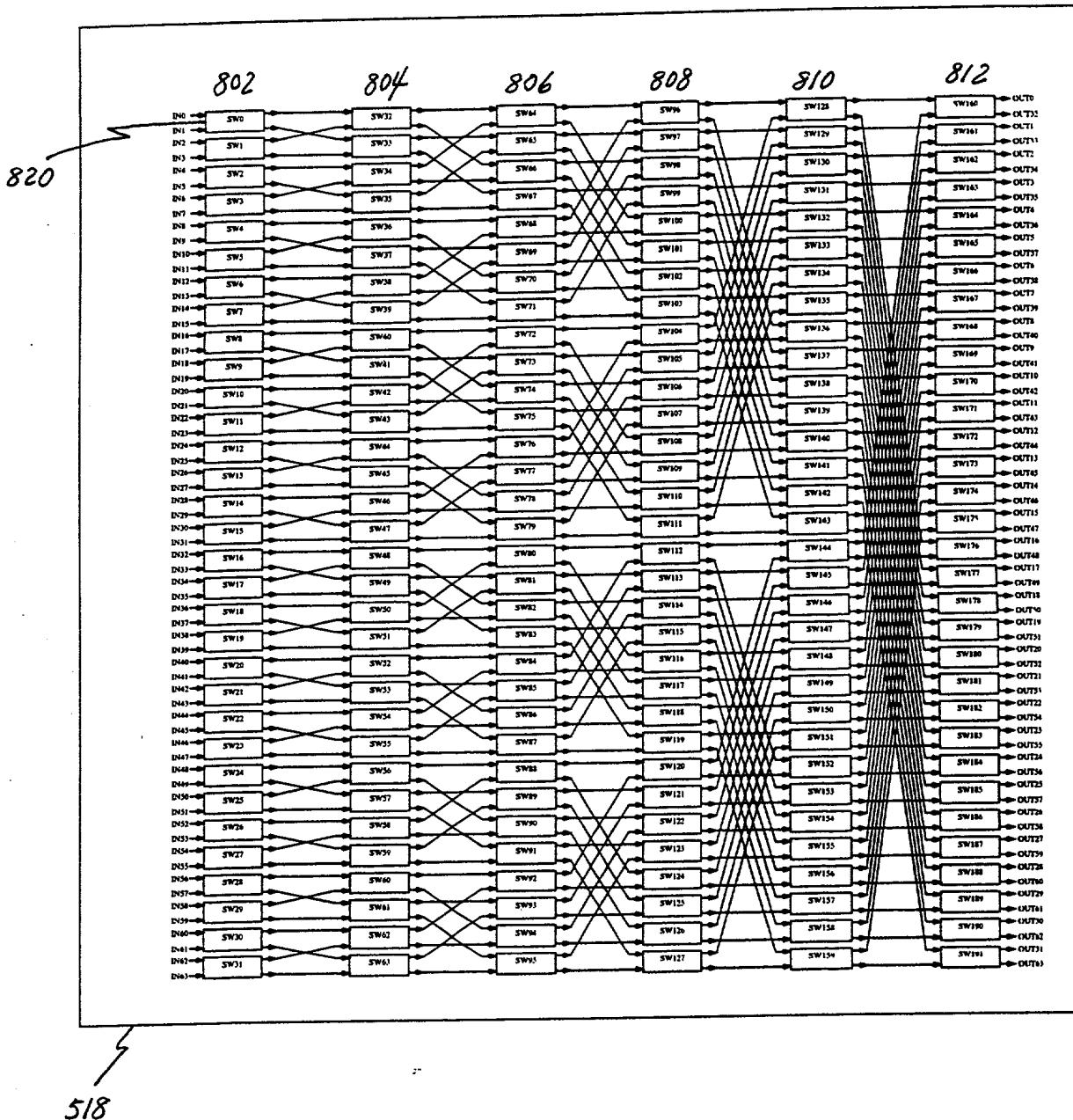


Fig. 8

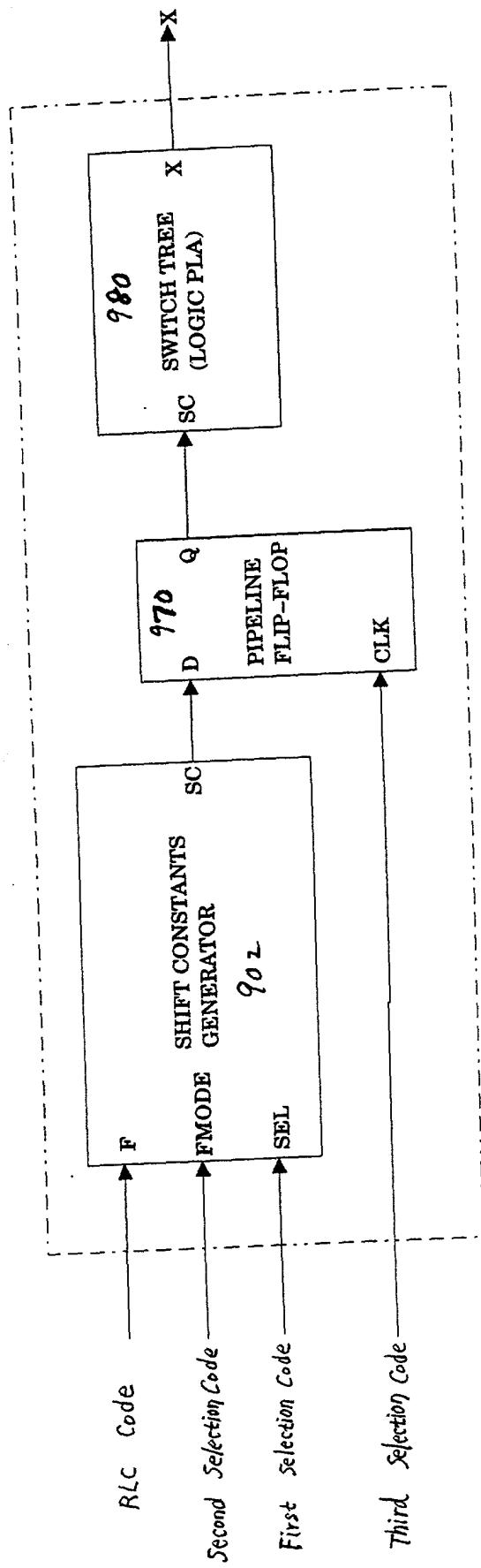
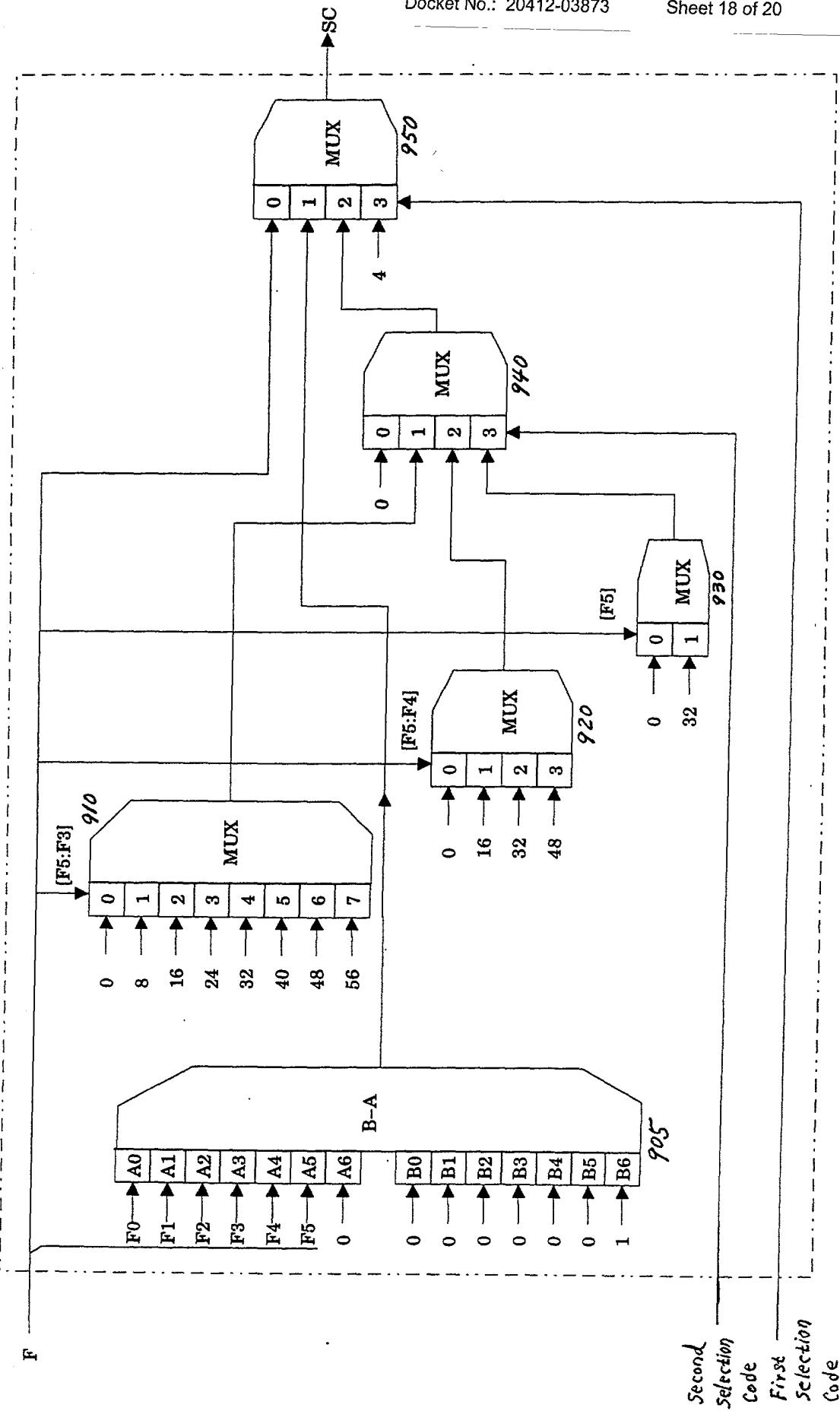
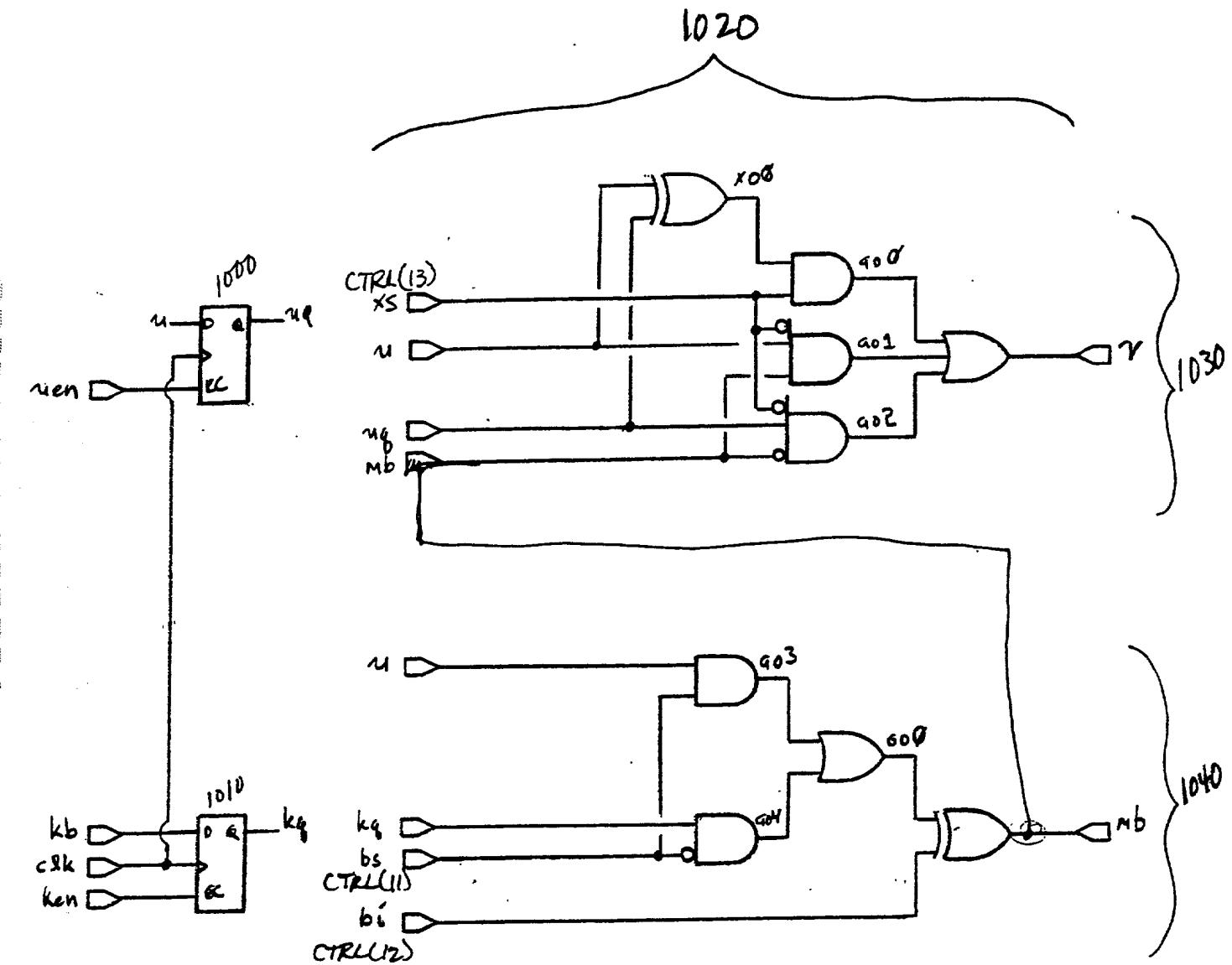


Fig. 9A

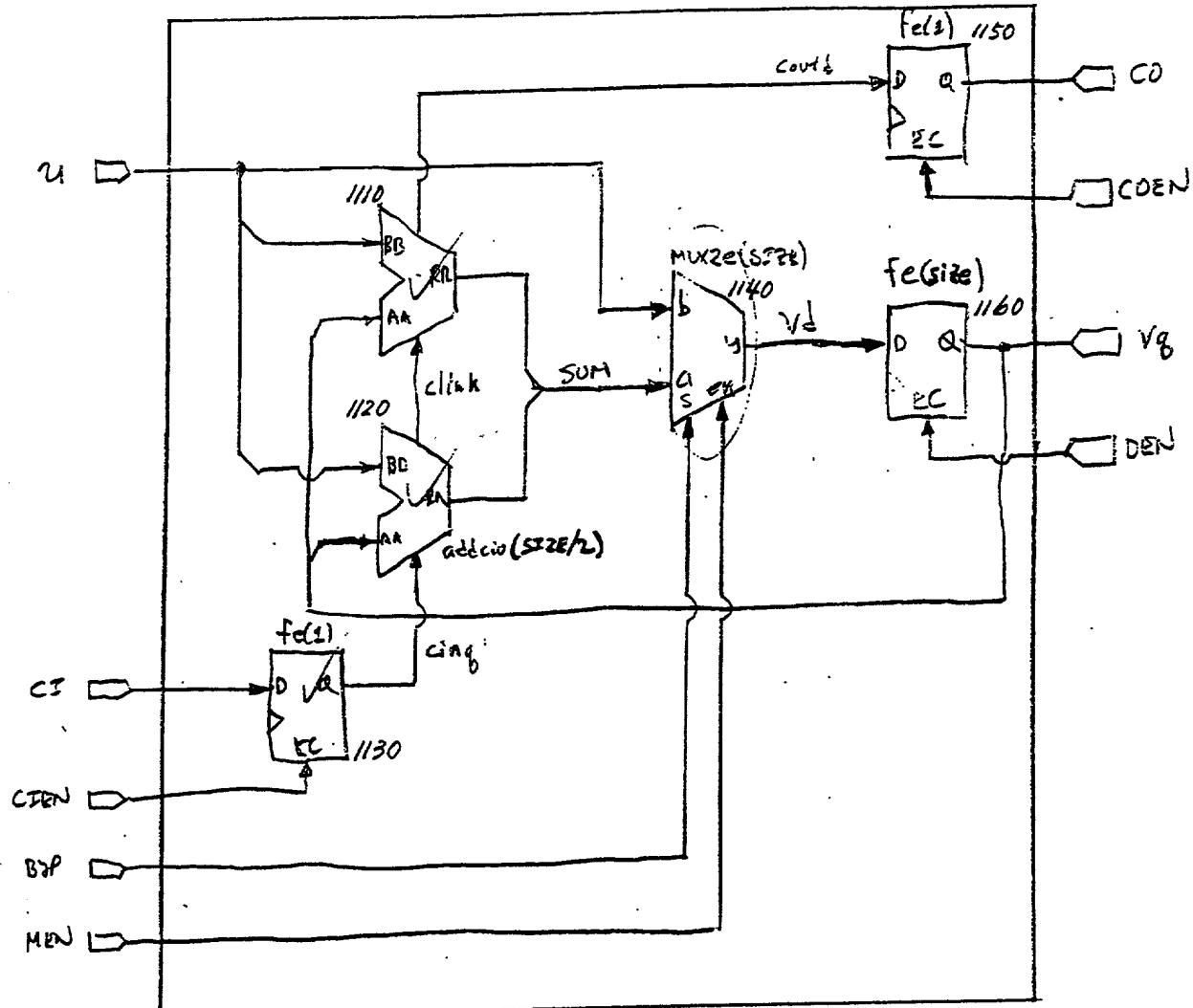
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Fig. 10



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Fig. 11

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